



Silicon Carbide Schottky Diode

Features

- Positive temperature coefficient
- Temperature-independent switching
- Maximum working temperature at 175 °C
- Unipolar devices and zero reverse recovery current
- Zero forward recovery voltage
- Essentially no switching losses
- Reduction of heat sink requirements
- High-frequency operation
- Reduction of EMI

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

Package: ITO-220AC

Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free

Terminals: Tin plated leads

Polarity: As marked

Maximum Ratings ($T_C=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE
Device marking code			D106506FQG2
Reverse voltage (repetitive peak) @ $T_j=25^\circ\text{C}$		V	



YJD106506FQG2

Electrical Characteristics

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	Typ.	Max.
Forward voltage drop	V_F	V	$I_F=6A, T_J=25^{\circ}C$	1.31	1.5
			$I_F=6A, T_J=175^{\circ}C$	1.65	-
Reverse leakage current	I_R	μA	$V_R=650V, T_J=25^{\circ}C$	0.5	25
			$V_R=650V, T_J=175^{\circ}C$	5	-
Total capacitive charge	Q_C	nC	$V_R=400V, T_J=25^{\circ}C, Q_C=\int_0^{V_R} I_C(V)dV$	25	-
Total capacitance	C	μF	$V_R=0V, f=1MHz$	378	-
			$V_R=200V, f=1MHz$	51	-
			$V_R=400V, f=1MHz$	49	-
Capacitance Stored Energy	E_C	μJ	$V_R=400V$	3	-

Thermal Characteristics ($T_a=25^{\circ}C$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	R_{J-C}	$^{\circ}C/W$	4.76

Typical Characteristics

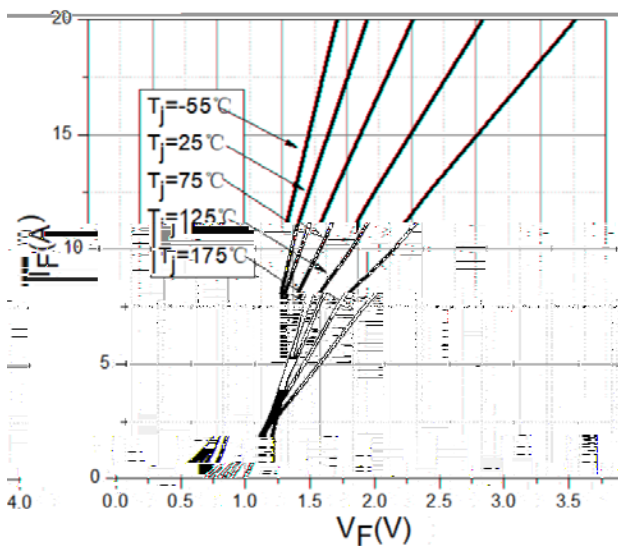


Figure 1. Forward Characteristics

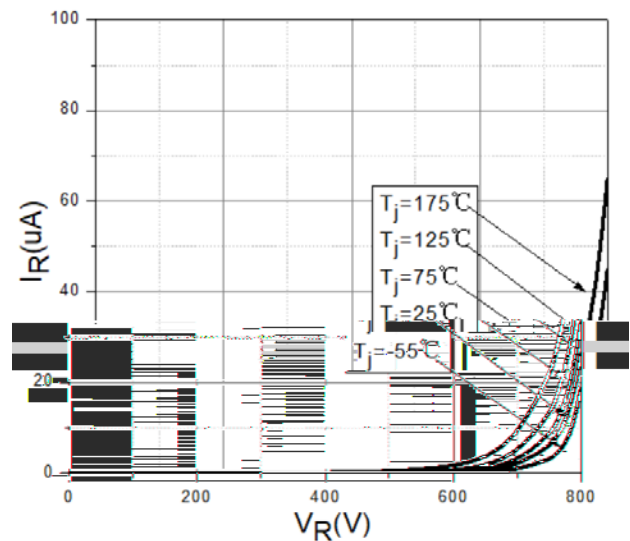


Figure2. Reverse Characteristic



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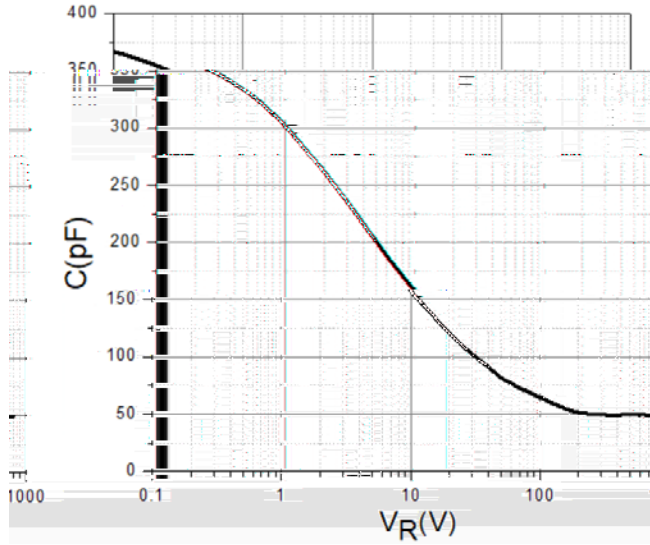


Figure 3. Capacitance vs. Reverse Voltage

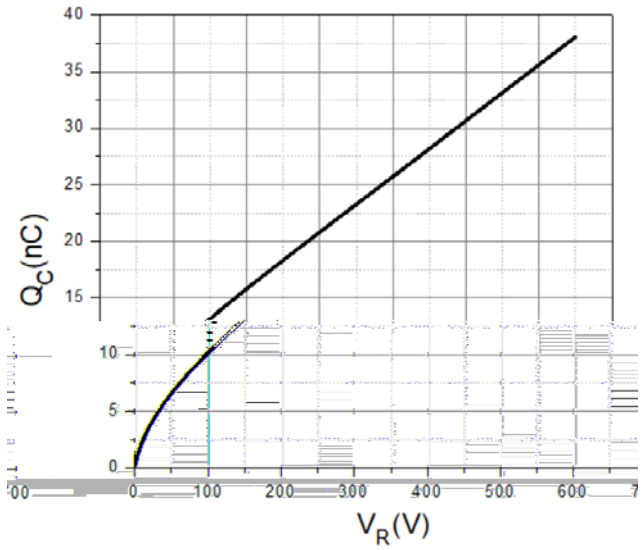


Figure 4. Total Capacitance Charge vs. Reverse Voltage

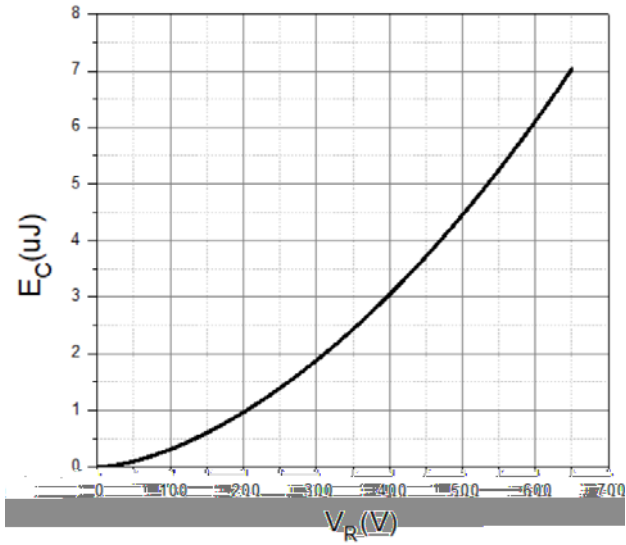


Figure 5. Capacitance Stored Energy

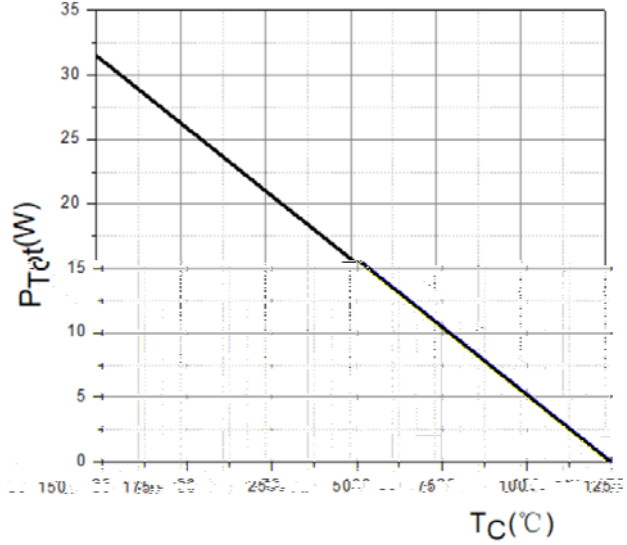


Figure 6. Power Derating

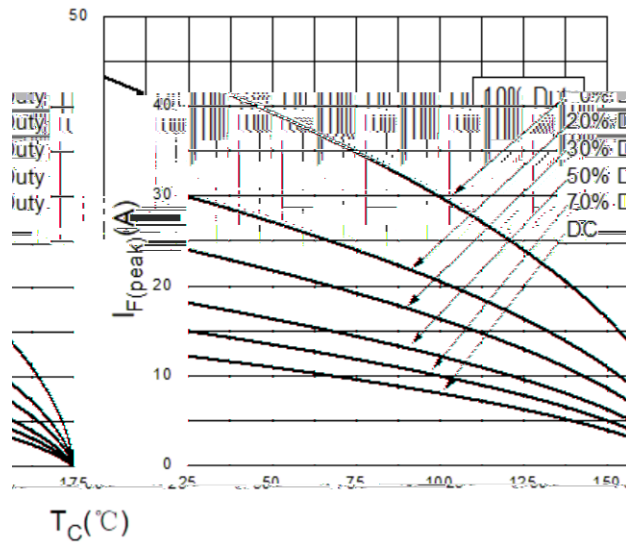


Figure 7. Current Derating

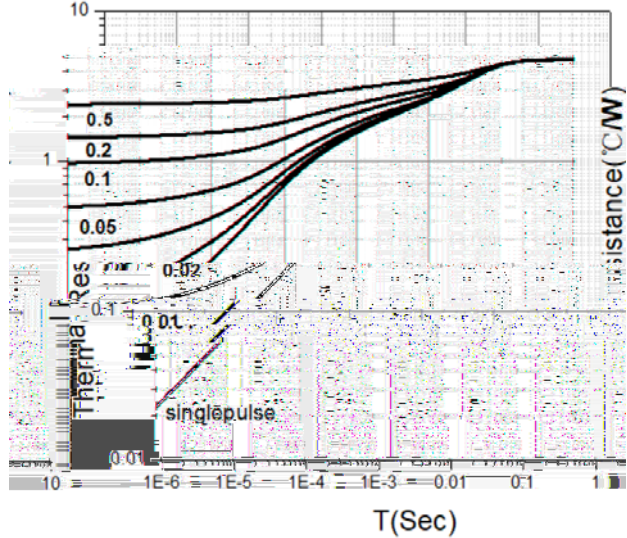
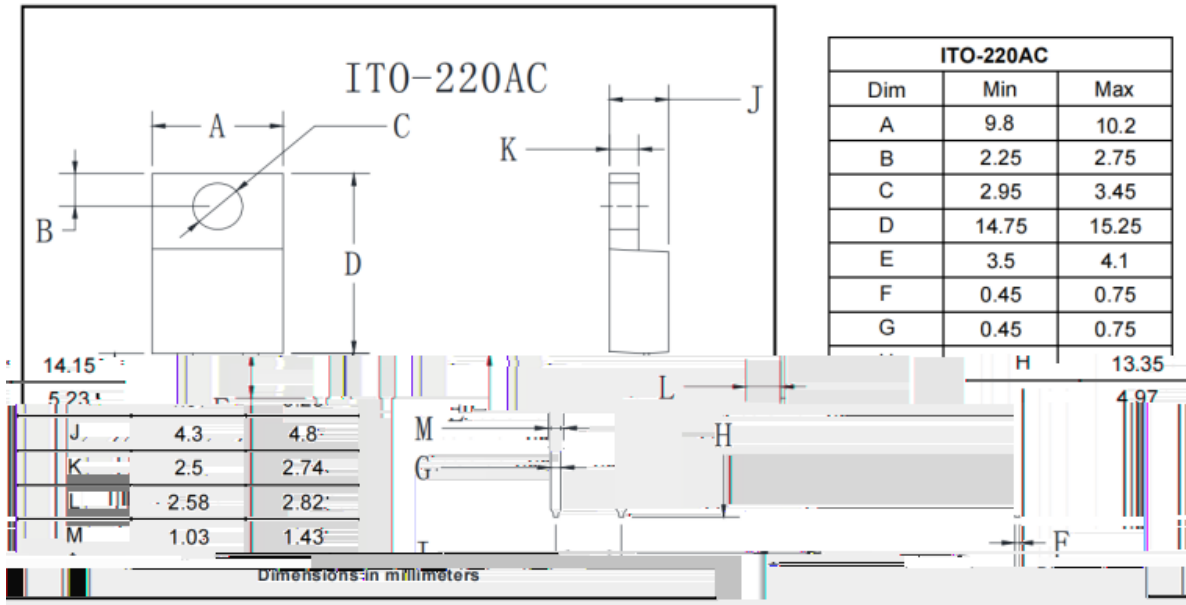


Figure 8. Transient Thermal Impedance



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Outline Dimensions





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